

IN THE SPECIFICATION:

Please replace paragraph [0030] with the following amended paragraph:

[0030] Transistors MP2 and MN1 operate as compensating current sources, with their sourced current, I_{CN} and I_{CP} , respectively, determined by the other transistors in their respective current mirror circuits. For example, the current I_{CN} flowing through MP2 mirrors the current I_{CN}' flowing through MNC1, according to the following equation:

$$I_{CN} = A_1 \times I_{CN}'$$

where A_1 is a constant, generally determined by the aspect ratio (ratio of channel width W to length L) of transistors MPC1 and MP2. A bias voltage VR_N may be used to control the current ~~I_{CN}~~ I_{CN}' by varying the gate-source voltage of MNC1, and may be selected according to the amount of current I_{CN} needed to compensate for changes in I_{N1} due to process variations.